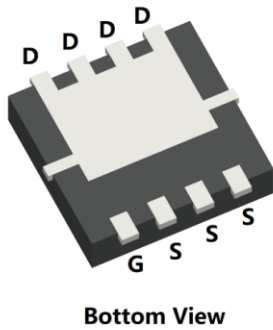
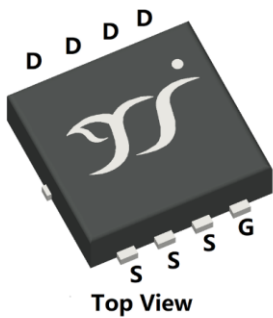
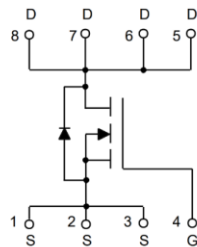


N-Channel Enhancement Mode Field Effect Transistor



PDFN3333-8L



Product Summary

• V_{DS}	60V
• I_D	60A
• $R_{DS(ON)}$ (at $V_{GS}=10V$)	$<7.5m\Omega$
• $R_{DS(ON)}$ (at $V_{GS}=4.5V$)	$<12.4m\Omega$
• 100% EAS Tested	
• 100% ∇V_{DS} Tested	

General Description

- Excellent package for heat dissipation
- High density cell design for low $R_{DS(ON)}$
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- DC-DC Converters
- Power management functions
- Industrial and Motor Drive application

Limiting Values

Parameter	Conditions		Symbol	Min	Max	Unit
Drain-source Voltage			V _{DS}	-	60	V
Gate-source Voltage			V _{GS}	-20	20	
Continuous Drain Current (Note 1,2)	Steady-State	T _A =25°C,V _{GS} =10V	I _D	-	11.9	A
		T _A =100°C,V _{GS} =10V		-	7.5	
Continuous Drain Current (Note 1,3)	Steady-State	T _C =25°C,V _{GS} =10V,Chip limitation		-	60	
		T _C =100°C,V _{GS} =10V		-	38	
Pulsed Drain Current	T _C =25°C,t _p ≤10μs		I _{DM}	-	240	
Maximum Body-Diode Continuous Current	T _C =25°C		I _S		45	
Avalanche Energy (non-repetitive)	T _J =25°C,V _G =10V, R _G =25Ω, L=0.5mH, I _{AS} =17A		EAS	-	72.2	mJ
Total Power Dissipation (Note 1,2)	Steady-State	T _A =25°C	P _D	-	1.92	W
		T _A =100°C		-	0.76	
Total Power Dissipation (Note 1,3)	Steady-State	T _C =25°C		-	50	
		T _C =100°C		-	20	
Junction and Storage Temperature Range			T _J , T _{STG}	-55	150	°C

Thermal Resistance

Parameter		Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient (Note 2)	Steady-State	$R_{\theta JA}$	-	65	$^{\circ}C/W$
Thermal Resistance Junction-to-Case	Steady-State	$R_{\theta JC}$	-	2.5	

Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJQ7D5G06AJ	F1	7D5G06A	5000	10000	100000	13" reel



YJQ7D5G06AJ

■ Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A, T_j=25^{\circ}C$	60	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=60V, V_{GS}=0V, T_j=25^{\circ}C$	-	-	1	μA
		$V_{DS}=60V, V_{GS}=0V, T_j=150^{\circ}C$	-	-	100	
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V, T_j=25^{\circ}C$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A, T_j=25^{\circ}C$	1.4	1.9	2.4	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=25A, T_j=25^{\circ}C$	-	6	7.5	m Ω
		$V_{GS}=4.5V, I_D=18A, T_j=25^{\circ}C$	-	8.3	12.4	m Ω
Diode Forward Voltage	V_{SD}	$I_S=25A, V_{GS}=0V, T_j=25^{\circ}C$	-	0.86	1.2	V
Gate Resistance	R_G	$f=1MHz, T_j=25^{\circ}C$	-	2	-	Ω
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{DS}=30V, V_{GS}=0V, f=1MHz, T_j=25^{\circ}C$	-	1270	-	μF
Output Capacitance	C_{oss}		-	300	-	
Reverse Transfer Capacitance	C_{rss}		-	9	-	
Switching Parameters						
Total Gate Charge	Q_g	$V_{GS}=10V, V_{DS}=30V, I_D=25A, T_j=25^{\circ}C$	-	21.5	-	nC
Gate-Source Charge	Q_{gs}		-	4.3	-	
Gate-Drain Charge	Q_{gd}		-	4.1	-	
Reverse Recovery Charge	Q_{rr}	$I_F=25A, di/dt=100A/\mu s, V_{GS}=0V, V_R=30V, T_j=25^{\circ}C$	-	11	-	nC
Reverse Recovery Time	t_{rr}		-	21	-	ns
Turn-on Delay Time	$t_{D(on)}$	$V_{GS}=10V, V_{DS}=30V, I_D=25A, R_{GEN}=2.7\Omega, T_j=25^{\circ}C$	-	10.3	-	ns
Turn-on Rise Time	t_r		-	52.2	-	
Turn-off Delay Time	$t_{D(off)}$		-	24.4	-	
Turn-off Fall Time	t_f		-	6.1	-	

Note:

1. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
2. The value of $R_{\theta JA}$ is measured with the device mounted on the 40mm*40mm*1.1mm single layer FR-4 PCB board with 1 in² pad of 2oz. Copper, in the still air environment with $T_A=25^\circ C$. The maximum allowed junction temperature of $150^\circ C$. The value in any given application depends on the user's specific board design.
3. Thermal resistance from junction to soldering point (on the exposed drain pad).



■ Typical Electrical and Thermal Characteristics Diagrams

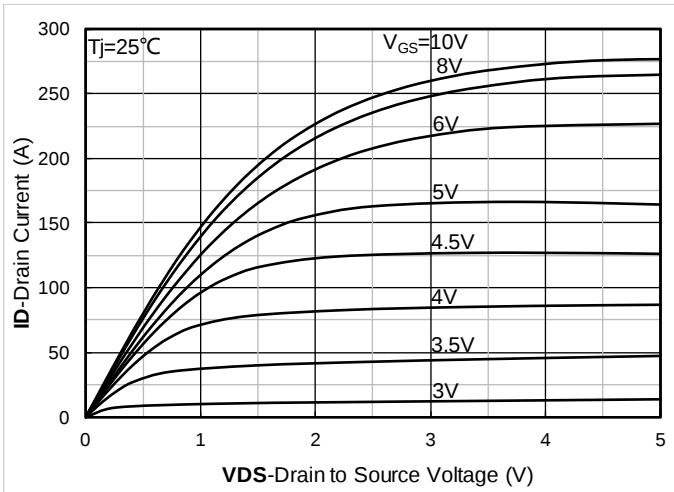


Figure 1. Output Characteristics; typical values

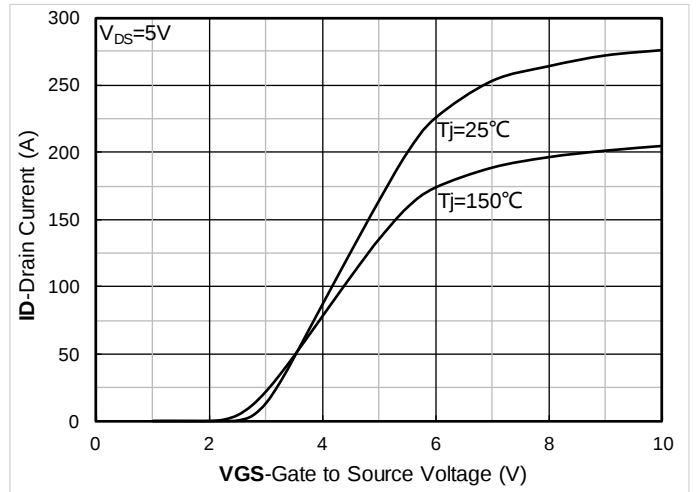


Figure 2. Transfer Characteristics; typical values

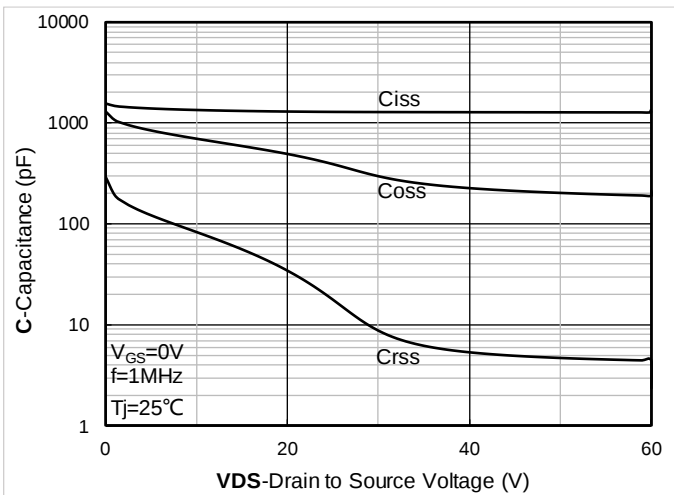


Figure 3. Capacitance Characteristics; typical values

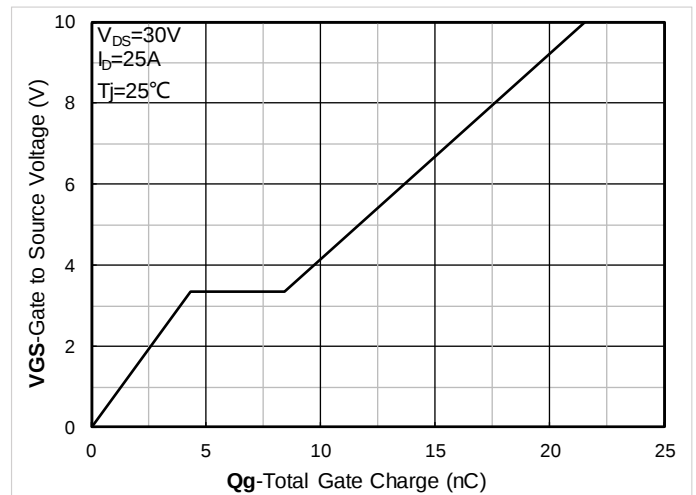


Figure 4. Gate Charge; typical values

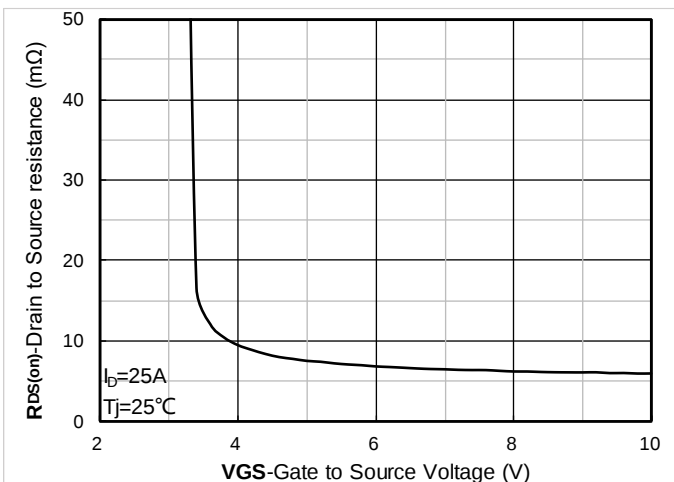


Figure 5. On-Resistance vs. Gate to Source Voltage; typical values

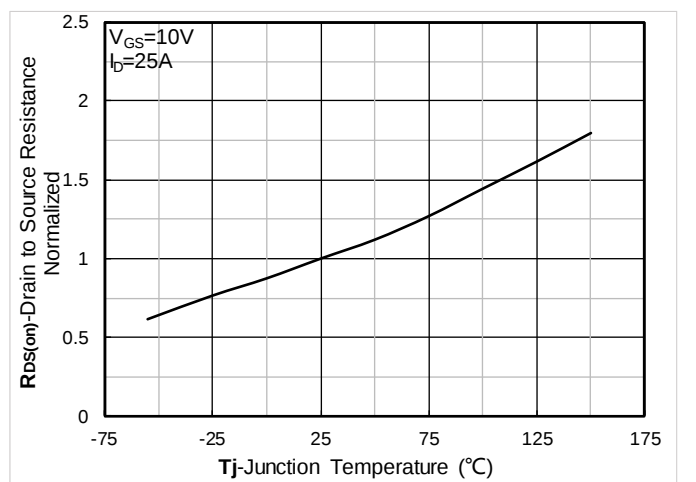


Figure 6. Normalized On-Resistance

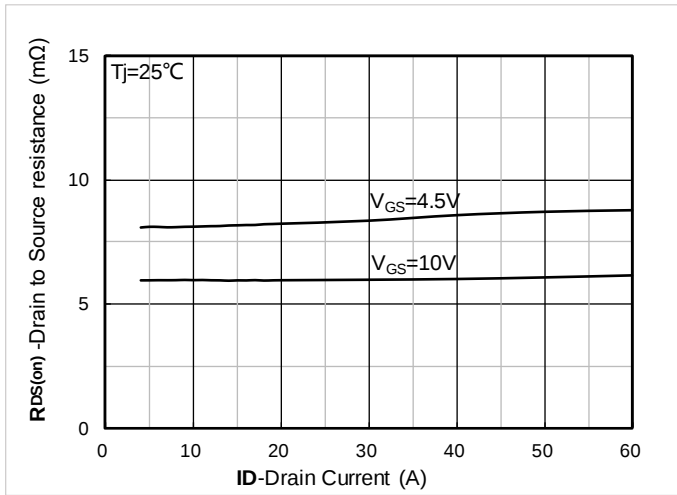


Figure 7. RDS(on) vs. Drain Current; typical values

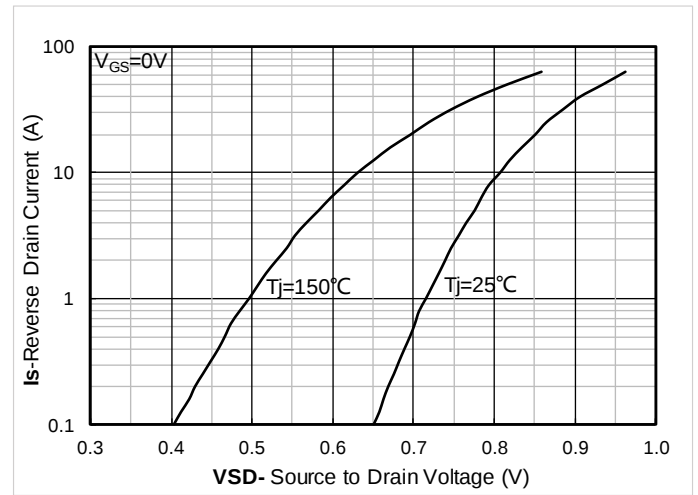


Figure 8. Forward characteristics of reverse diode; typical values

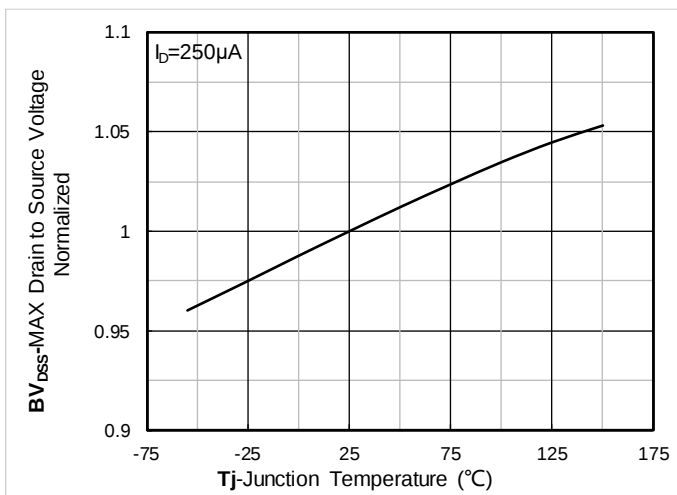


Figure 9. Normalized breakdown voltage

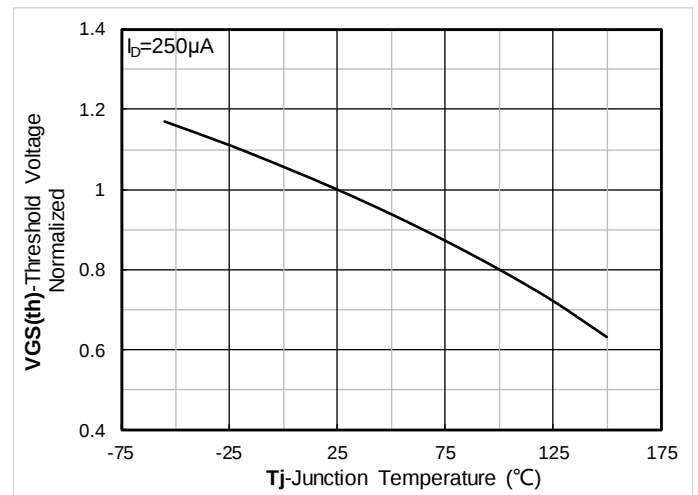


Figure 10. Normalized Threshold voltage

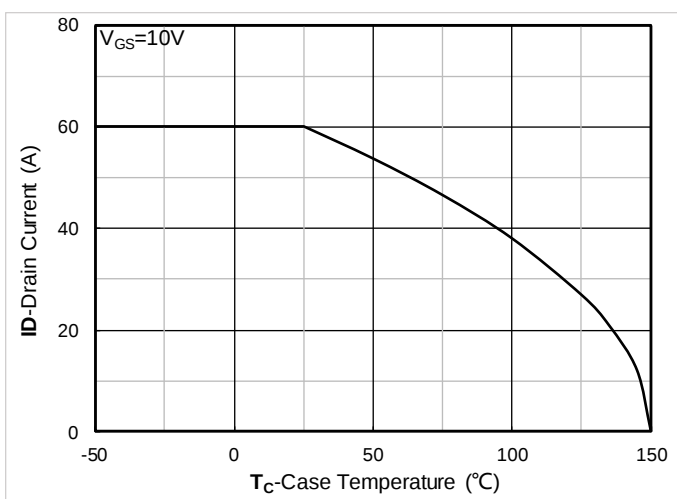


Figure 11. Current dissipation

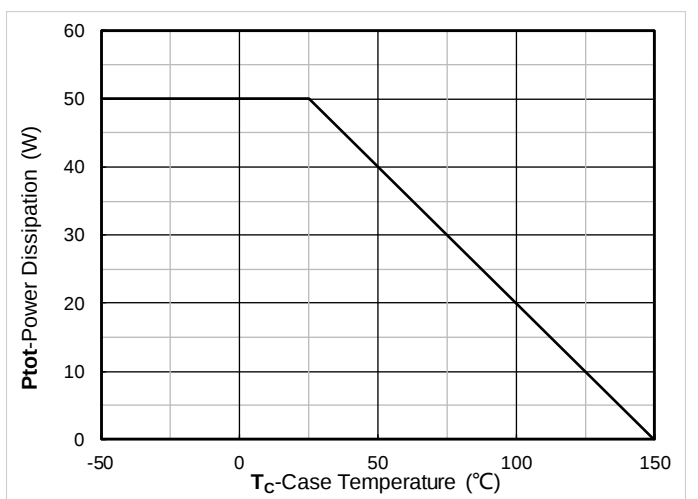


Figure 12. Power dissipation

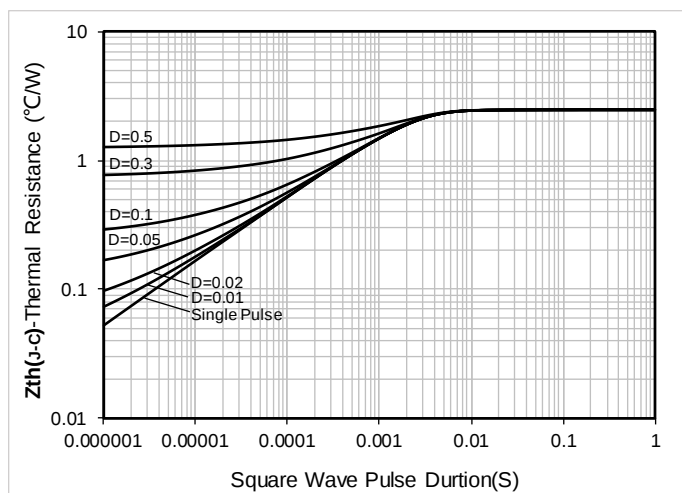


Figure 13. Maximum Transient Thermal Impedance

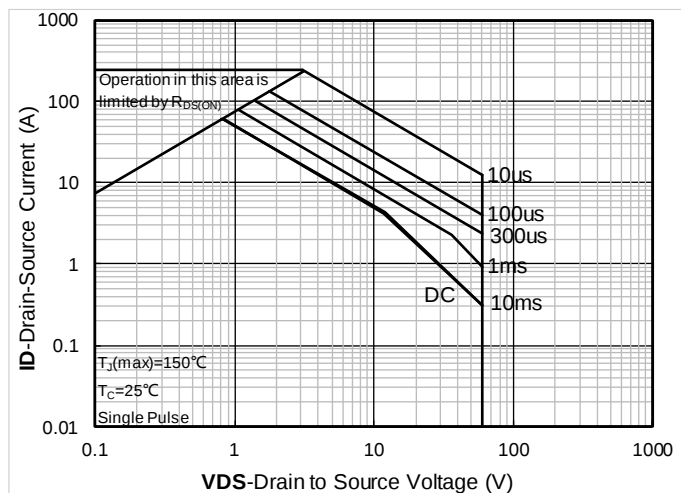


Figure 14. Safe Operation Area

■ Test Circuits & Waveforms

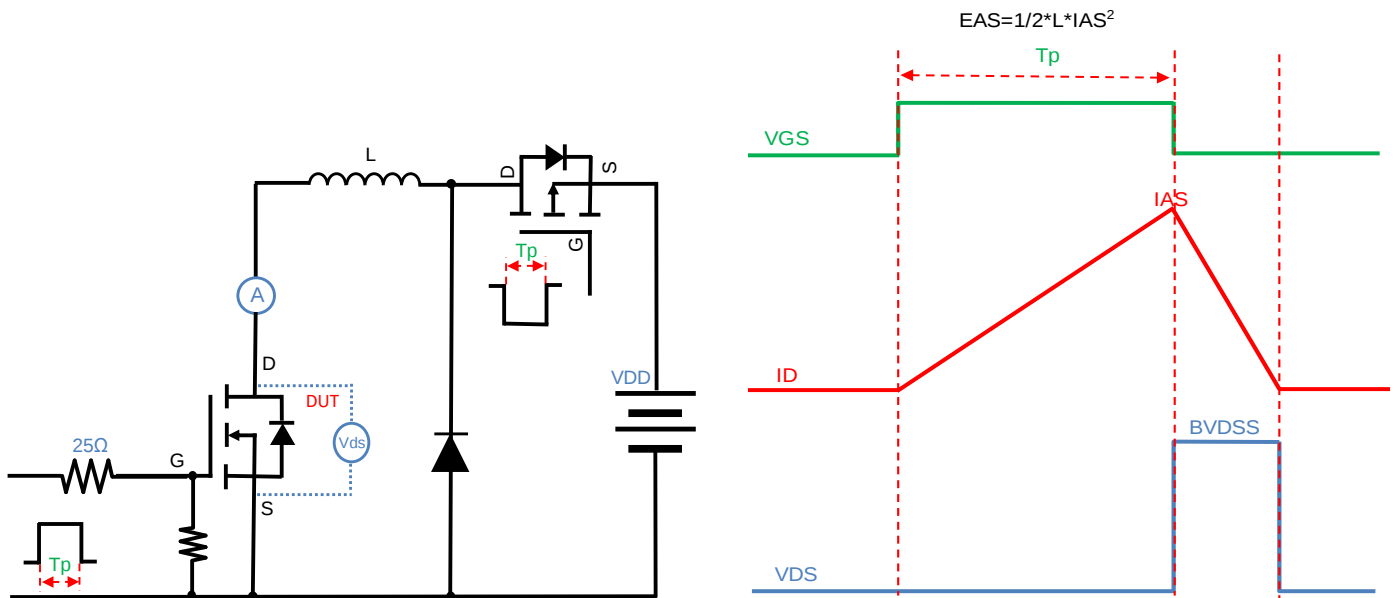


Figure A. Unclamped Inductive Switching (UIS) Test Circuit & Waveform

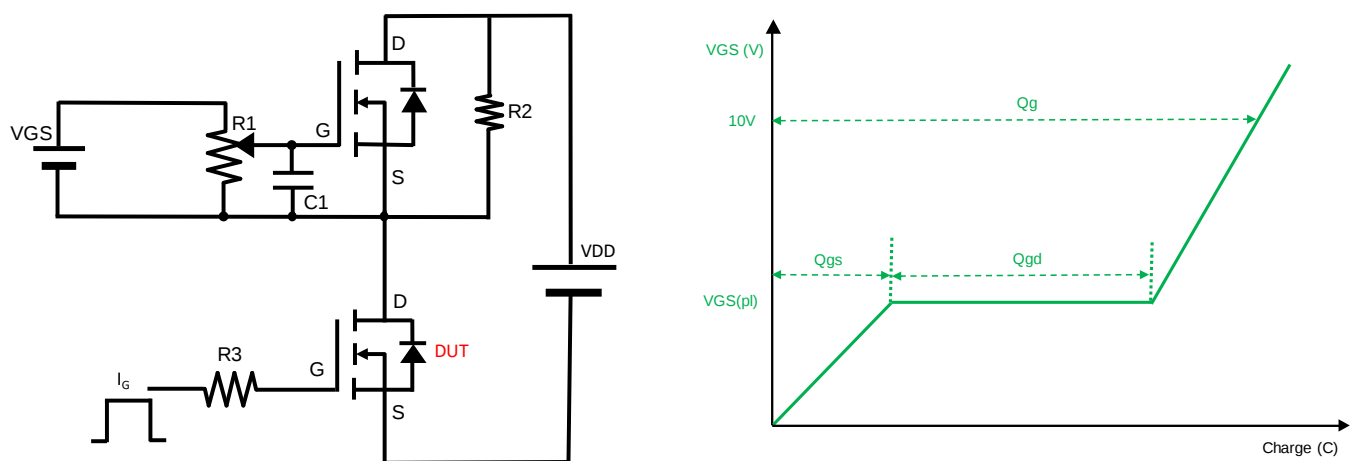


Figure B. Gate Charge Test Circuit & Waveform

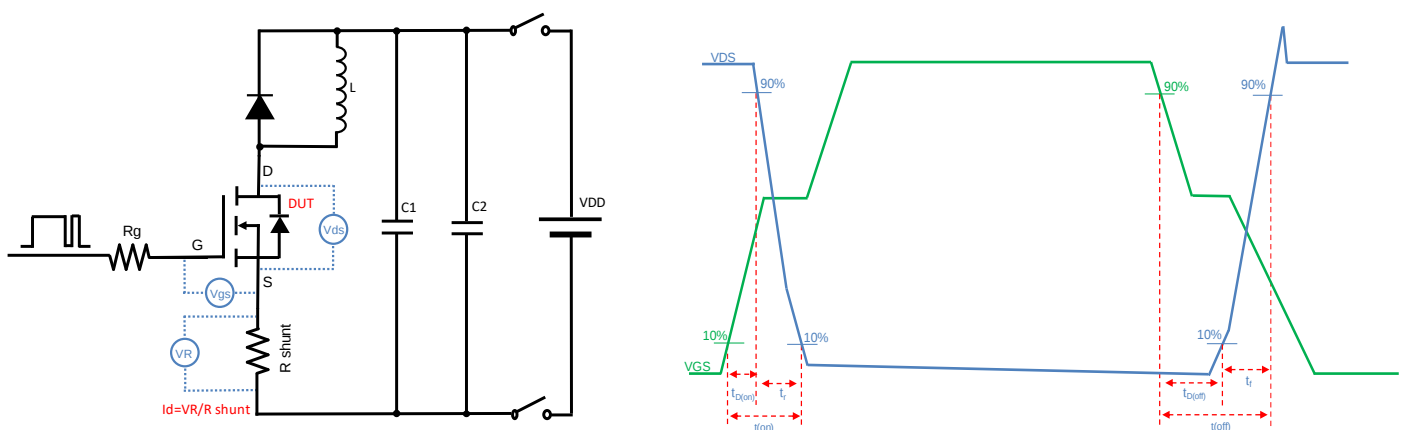


Figure C. Resistive Switching Test Circuit & Waveform

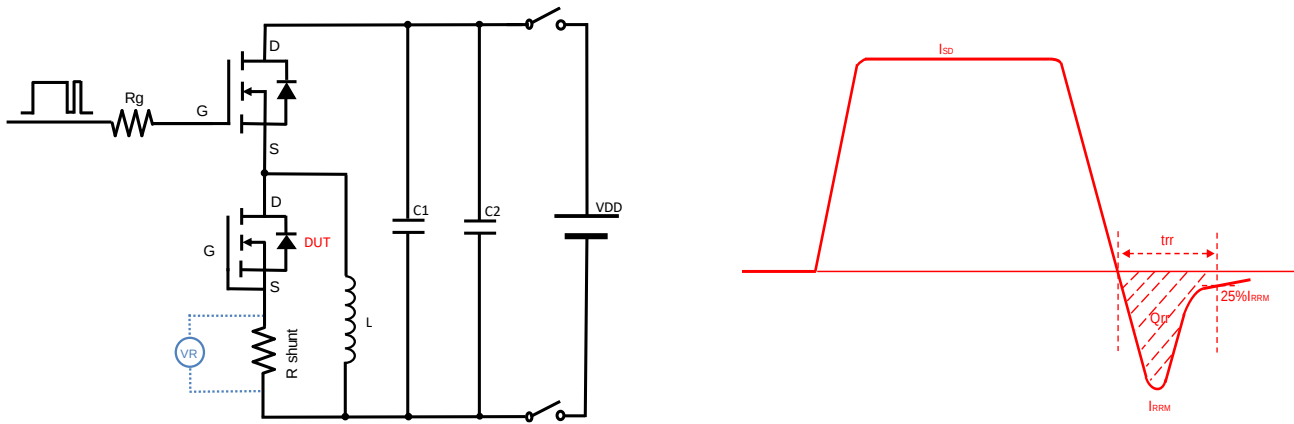
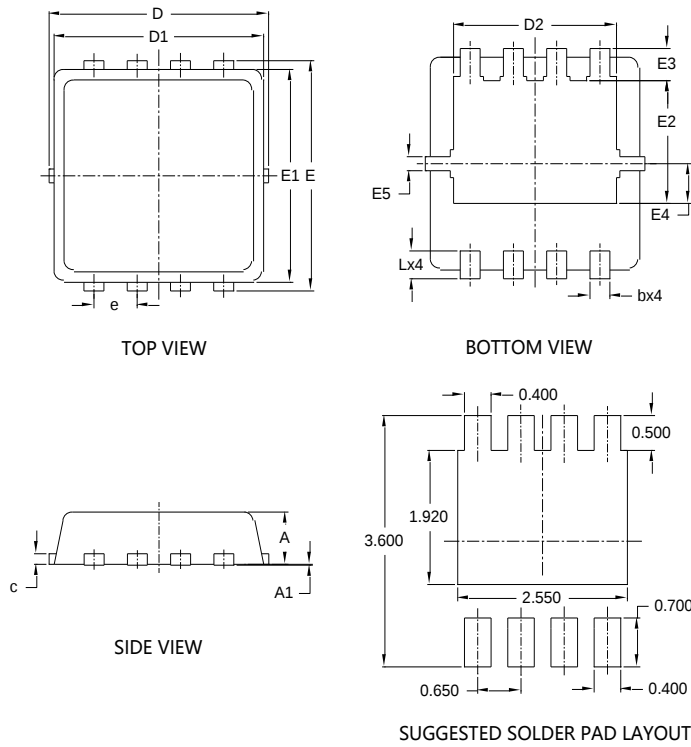


Figure D. Diode Recovery Test Circuit & Waveform



YJQ7D5G06AJ

■ PDFN3333-8L-A-0.75mm Package information



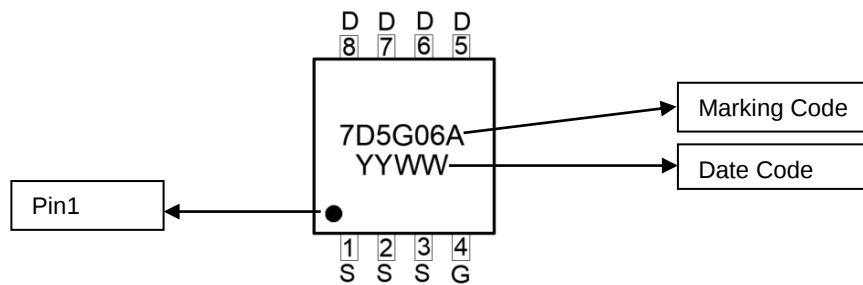
SYMBOL	DIMENSIONS			
	INCHES		Millimeter	
	MIN.	MAX.	MIN.	MAX.
A	0.028	0.033	0.700	0.850
A1	0.000	0.002	0.000	0.050
b	0.008	0.016	0.200	0.400
c	0.004	0.010	0.100	0.250
D	0.124	0.136	3.150	3.450
D1	0.118	0.130	3.000	3.300
D2	0.089	0.104	2.250	2.650
E	0.124	0.136	3.150	3.450
E1	0.114	0.126	2.900	3.200
E2	0.061	0.076	1.540	1.940
E3	0.011	0.026	0.280	0.650
E4	0.022 REF		0.570 REF	
E5	0.008 REF		0.200 REF	
e	0.026 BSC		0.650 BSC	
L	0.012	0.020	0.300	0.500

NOTE:

1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
2. TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.
3. THE PAD LAYOUT IS FOR REFERENCE PURPOSES ONLY.

UNIT: mm

■ Marking Information



Note:

1. All marking is at middle of the product body
2. All marking is in laser printing
3. 7D5G06A is marking code, YYWW is date code, "YY" is year, "WW" is week
4. Body color: Black



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The product listed herein is designed to be used with ordinary electronic equipment or devices, and not designed to be used with equipment or devices which require high level of reliability and the malfunction of which would directly endanger human life (such as medical instruments, transportation equipment, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), Yangjie or anyone on its behalf, assumes no responsibility or liability for any damages resulting from such improper use of sale.

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